O.P.Code: 18EE0227

R18

H.T.No.

SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR (AUTONOMOUS)

B.Tech IV Year I Semester Supplementary Examinations June-2024 POWER QUALITY

/m !	(Electrical & Electronics Engineering)			
11m	ne: 3 Hours	Max.	Mar	ks: 60
	PART-A			
1	(Answer all the Questions $5 \times 2 = 10$ Marks) a Define notching	~		
	b Define Sag?	CO ₁	L2	2M
		CO ₂	L1	2M
	c What is ment by harmonics?d What is revenue meters?	CO ₂	L2	2M
		CO ₃	L1	2M
	e What is Solid State Breaker?	CO ₄	L2	2M
	PART-B			
	(Answer all Five Units $5 \times 10 = 50 \text{ Marks}$)			
	UNIT-I			
2	What is power quality? Why we are concern about power quality?	CO ₁	L1	10M
	OR			
3	Draw and explain ITI curve.	CO ₁	L2	10M
	UNIT-II			
4	What are the sources of transient over voltages? Explain clearly.	CO ₂	L2	10M
	OR	-		101/1
5	Draw and explain the impulsive and oscillatory transients.	CO2	L1	10M
	UNIT-III			20112
6	What is harmonic distortion? Discuss about the voltage versus current	CO2	т 2	103/
	distortion.	COZ	L4	10M
	OR			
7	Explain the various devices for the controlling of harmonics distortion.	CO2	L1	10M
	UNIT-IV	COZ	LI	TOM
8	Explain the various power quality monitoring considerations.	COA	.	407.5
Ü	-	CO ₃	L1	10M
9	OR Explain about the power quality bench marking.	COA	T 4	403.5
		CO ₃	Ll	10M
10	UNIT-V			
10	Draw and explain the schematic diagram of a right shunt UPQC.	CO ₄	L3	10M
	OR			20
11	Explain the principle of DVR operation used for sag mitigation.	CO4	L1	10M
	*** END ***			

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